

10/538216

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

JC17 Rec'd PCT/PTO 09 JUN 2005
Atty. Docket

JURRIAAN SCHMITZ et al.

NL 021416

Serial No.

Group Art Unit

Filed: CONCURRENTLY

Ex.

VERTICAL INSULATED GATE TRANSISTOR AND MANUFACTURING METHOD

Commissioner for Patents
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination, please amend the above-identified application as follows: